



We remove the rubber, unscrew 4 screws

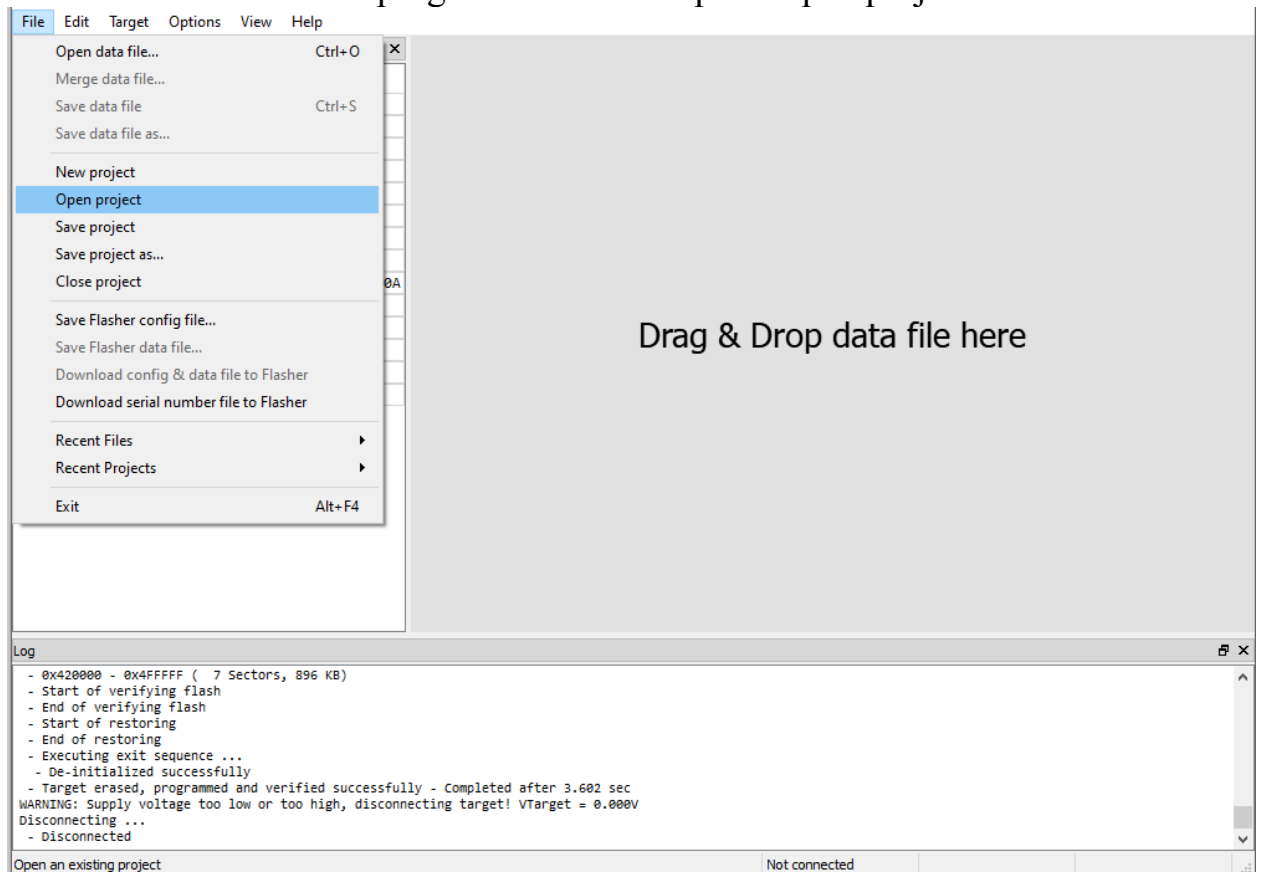


and again unscrew 4 screws

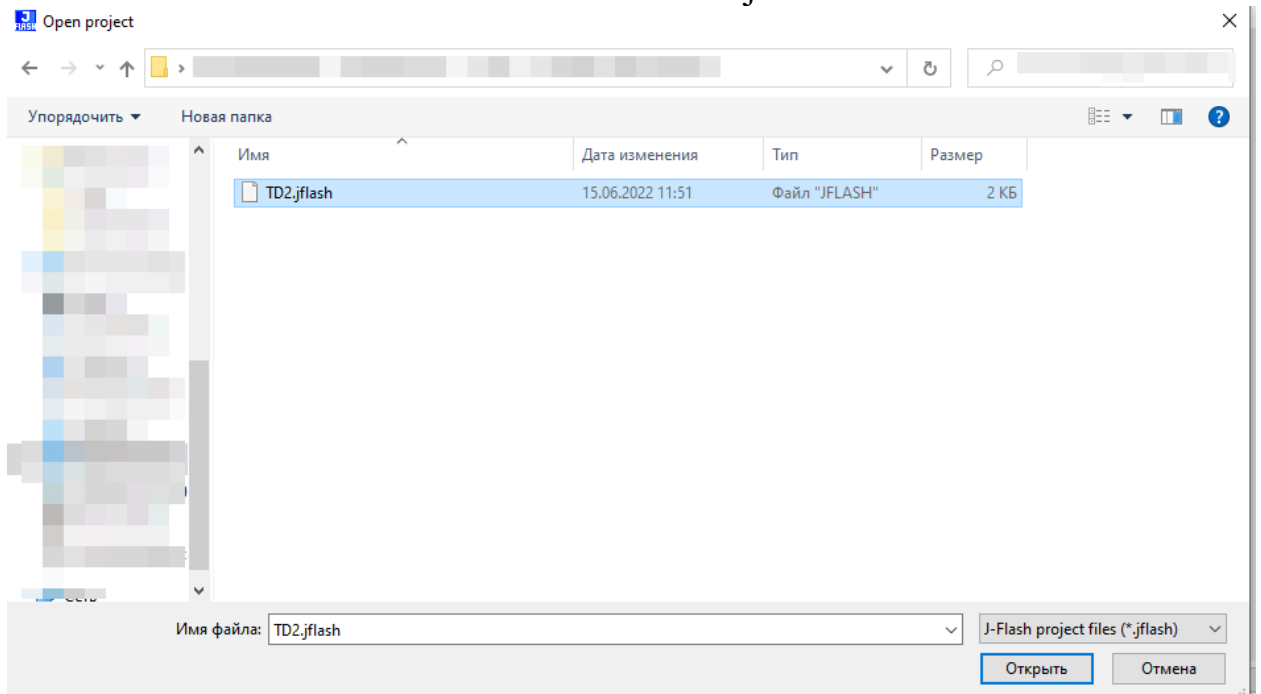


GND
SWCK/TCK
SWIO/TMS
3,3Vterf

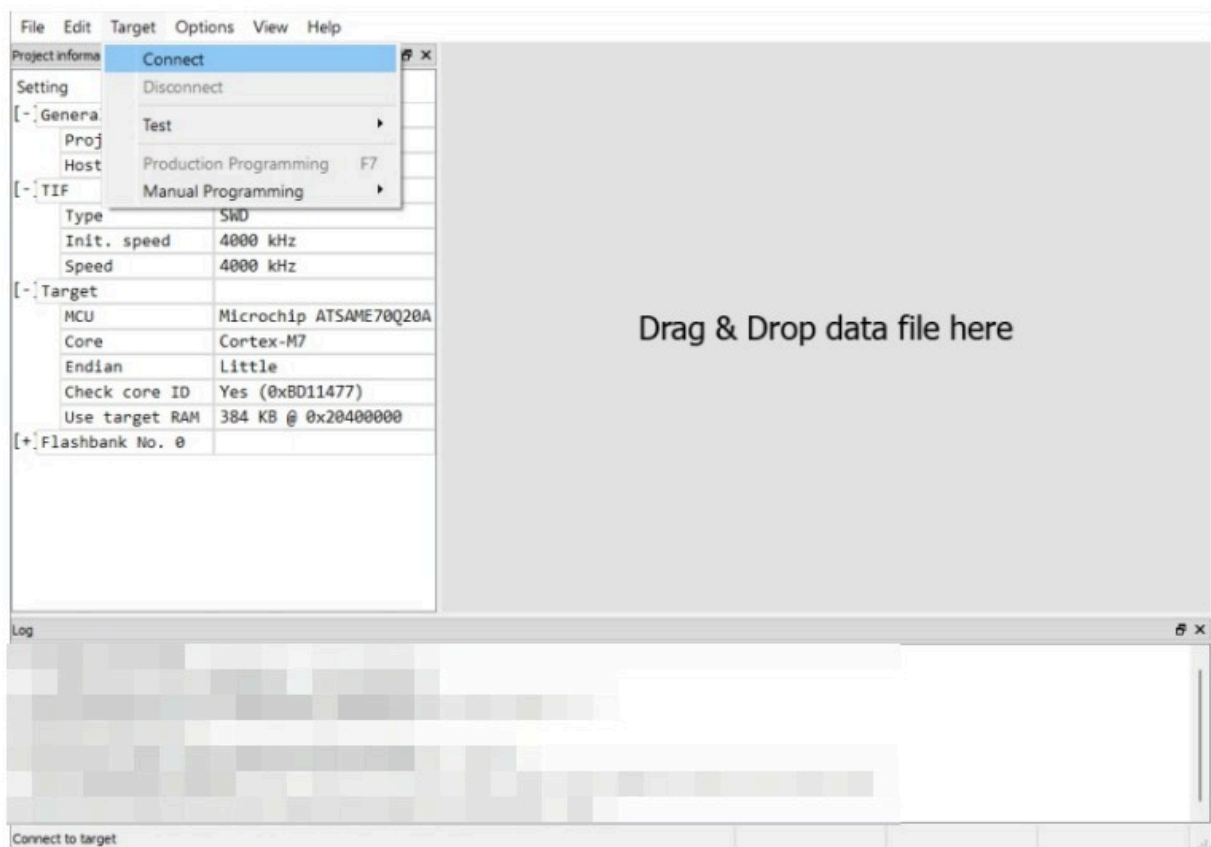
Connect to j-link V9, no lower version
Start program J-Flash and push Open project



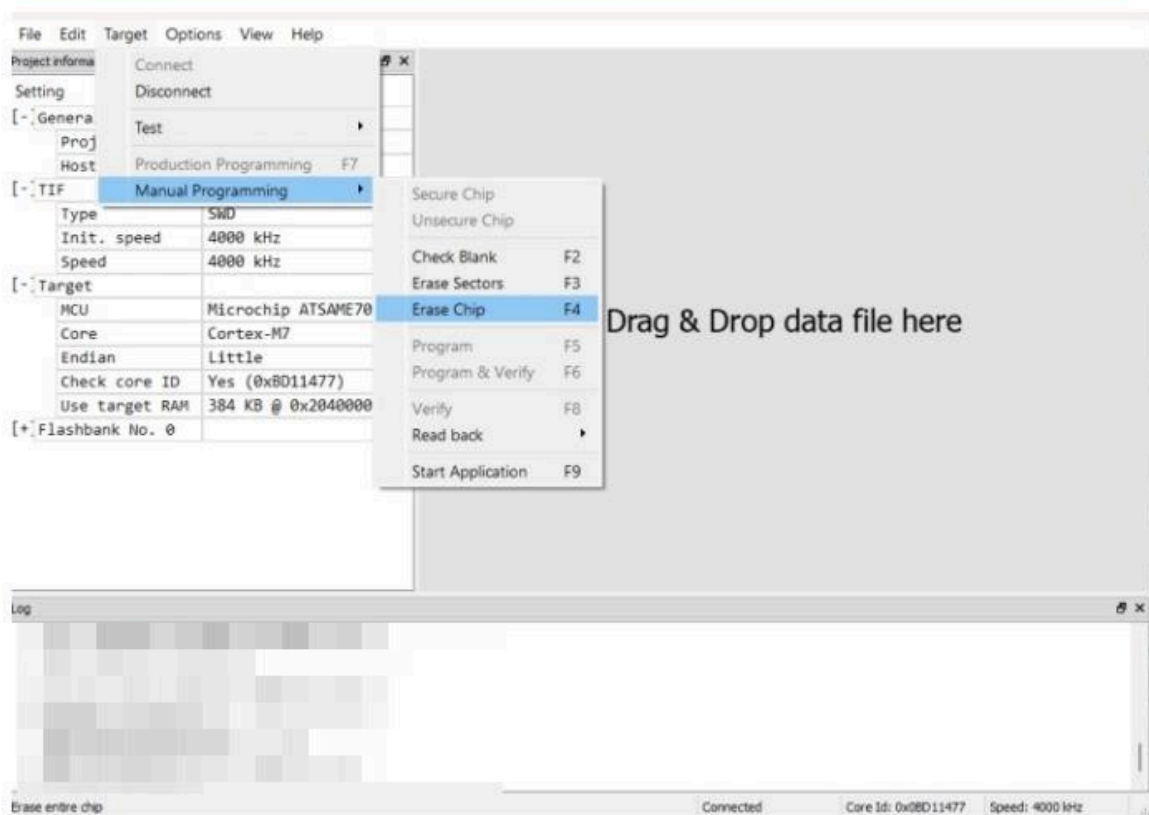
in folder select TD2.jflash



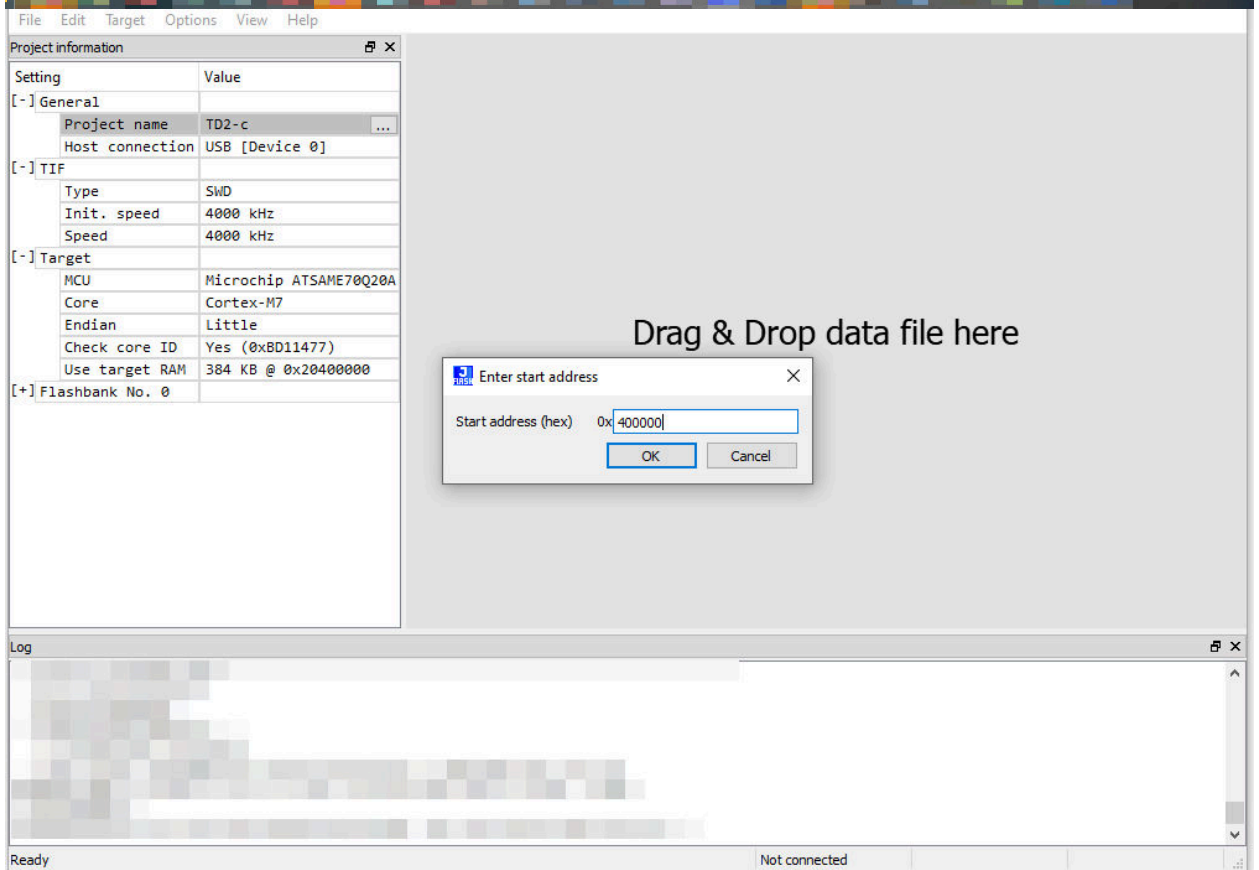
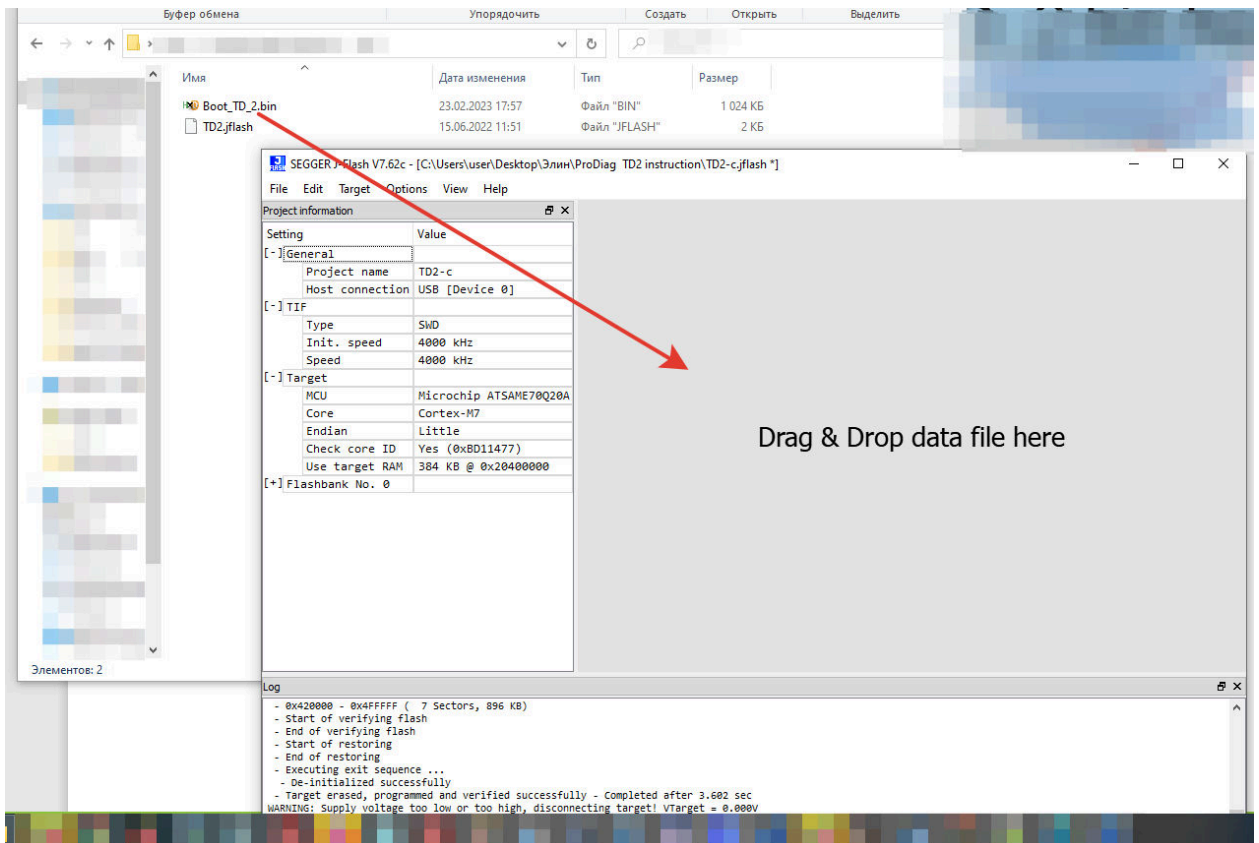
in program select Target/Connect



Now erase your chip: Go to Target/Manual Programming/Erase chip



From the TD2 flash folder, drag the file Boot_TD_2.bin here



OK Now go to : Target/Production Programming
If everything is fine, you will see this message

The screenshot displays the J-Flash V7.62c software interface. On the left, the 'Project information' panel shows settings for a project named 'TD2-c' connected via USB. The target is identified as a Microchip ATSAME70Q20A with a Cortex-M7 core. The programming speed is set to 4000 kHz. A modal dialog box in the center confirms: 'Target erased, programmed and verified successfully - Completed after 3.602 sec'. The main window shows a memory map with hexadecimal data. The bottom 'Log' panel provides a detailed summary of the programming process, including flash programming ranges and verification steps. The status bar at the bottom indicates the device is 'Ready', 'Connected', with 'Core Id: 0x0BD11477' and 'Speed: 4000 kHz'.

Project information

Setting	Value
[-] General	
Project name	TD2-c
Host connection	USB [Device 0]
[-] TIF	
Type	SWD
Init. speed	4000 kHz
Speed	4000 kHz
[-] Target	
MCU	Microchip ATSAME70Q20A
Core	Cortex-M7
Endian	Little
Check core ID	Yes (0x0BD11477)
Use target RAM	384 KB @ 0x204000
[+] Flashbank No. 0	

Go To: [1] [2] [4]

Memory Map:

Address	Hex Data
0040_0000	98 A3 40 20 23 CA 40 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0010	45 CA 40 00 45 CA 40 00 45 CA 40 00 00 00 00 00 00 00
0040_0020	00 00 00 00 00 00 00 00 00 00 00 00 45 CA 40 00
0040_0030	45 CA 40 00 00 00 00 00 45 CA 40 00 45 CA 40 00
0040_0040	45 CA 40 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0050	45 CA 40 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0060	45 CA 40 00 00 00 00 00 19 99 40 00 25 99 40 00
0040_0070	31 99 40 00 45 CA 40 00 2D 97 40 00 45 CA 40 00
0040_0080	3D 99 40 00 49 99 40 00 45 CA 40 00 45 CA 40 00
0040_0090	45 CA 40 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_00A0	49 97 40 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_00B0	45 CA 40 00 D6 9C 40 00 45 CA 40 00 45 CA 40 00
0040_00C0	45 CA 40 00 45 CA 40 00 0D 03 40 00 45 CA 40 00
0040_00D0	00 00 00 00 45 CA 40 00 00 00 00 00 45 CA 40 00
0040_00E0	00 00 00 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_00F0	00 00 00 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0100	00 00 00 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0110	00 00 00 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0120	00 00 00 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0130	00 00 00 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0140	00 00 00 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0150	00 00 00 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0160	00 00 00 00 45 CA 40 00 45 CA 40 00 45 CA 40 00
0040_0170	11 60 70 47 01 21 04 4A 11 60 70 47 01 21 03 4A
0040_0180	11 60 70 47 1C 12 41 20 20 12 41 20 84 21 40 20
0040_0190	04 49 40 B2 51 F8 20 00 70 47 02 4A 40 B2 42 F8
0040_01A0	20 10 70 47 90 18 0E 40 01 49 01 60 70 47 00 00
0040_01B0	01 00 00 A5 70 47 00 00 44 4F 57 4E 4C 4F 41 44
0040_01C0	2E 42 49 4E 00 00 00 00 00 00 00 00 00 00 00 00
0040_01D0	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0040_01E0	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0040_01F0	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

Log

- Flash programming performed for 3 ranges (1048576 bytes)
- 0x400000 - 0x403FFF (2 Sectors, 16 KB)
- 0x404000 - 0x41FFFF (1 Sector, 112 KB)
- 0x420000 - 0x4FFFFFF (7 Sectors, 896 KB)
- Start of verifying flash
- End of verifying flash
- Start of restoring
- End of restoring
- Executing exit sequence ...
- De-initialized successfully
- Target erased, programmed and verified successfully - Completed after 3.602 sec

Status Bar: Ready Connected Core Id: 0x0BD11477 Speed: 4000 kHz